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MAY 25 2005

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Gerry Marrero	(Depositor's name)
<i>Gerry Marrero</i>	(Signature)
5/23/05	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,462	07/03/2003	William B. Andrews	1054.025	6540

TITLE OF INVENTION: PROGRAMMABLE I/O INTERFACES FOR FPGAS AND OTHER PLDS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	06/30/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
CHANG, DANIEL D	2819		326-046000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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1. Steve Mendelsohn

2. _____

3. _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Lattice Semiconductor Corporation

Hillsboro, Oregon (USA)

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Date 5/23/05

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